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| | Application No. | Applicant(s) | • | |
| Notice of Allowability | 10/775,328 | KATO ET AL. | | |
| | Examiner | Art Unit | | |
| | Steven J. Fulk | 2891 | | |
| The MAILING DATE of this communication appeals claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.313 1. This communication is responsive to the amendment filed. | (OR REMAINS) CLOSED in or other appropriate comm IGHTS. This application is a and MPEP 1308. | n this application. If not included unication will be mailed in due co | urse THIS | |
| 2. ☑ The allowed claim(s) is/are <u>1-11 and 16-27</u> . | • | | | |
| 3. Acknowledgment is made of a claim for foreign priority unal priority and all blue blue blue blue blue blue blue bl | e been received. | | | |
| 3. ☐ Copies of the certified copies of the priority do | | | n from the | |
| International Bureau (PCT Rule 17.2(a)). | | a and national olage application | | |
| * Certified copies not received: | | | | |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | MENT of this application. | | | |
| A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which giv | nitted. Note the attached EX es reason(s) why the oath o | AMINER'S AMENDMENT or NO r declaration is deficient. | FICE OF | |
| 5. CORRECTED DRAWINGS (as "replacement sheets") mu | st be submitted. | | | |
| (a) \square including changes required by the Notice of Draftspers | son's Patent Drawing Revie | w (PTO-948) attached | • | |
| 1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date | | | | |
| (b) ☐ including changes required by the attached Examiner Paper No./Mail Date | s Amendment / Comment o | r in the Office action of | | |
| Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the | l.84(c)) should be written on t the header according to 37 C | the drawings in the front (not the baFR 1.121(d). | ack) of | |
| DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT | osit of BIOLOGICAL MAT FOR THE DEPOSIT OF BI | ERIAL must be submitted. No OLOGICAL MATERIAL. | te the | |
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| Attachment(s) | | | | |
| 1. ☐ Notice of References Cited (PTO-892) | 5. Notice of Ir | nformal Patent Application | | |
| 2. Notice of Draftperson's Patent Drawing Review (PTO-948) | 6. Interview S | Summary (PTO-413), | | |
| 3. M Information Disclosure Statements (PTO/SB/08), | 7. 🔲 Examiner's | ./Mail Dates Amendment/Comment | | |
| Paper No./Mail Date <u>7/23/07</u> 4. Examiner's Comment Regarding Requirement for Deposit | 8 □ Examiner's | Statement of Reasons for Allow | ance | |
| of Biological Material | | 9. | | |
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Application/Control Number: 10/775,328

Art Unit: 2891

DETAILED ACTION

Page 2

Continued Examination Under 37 CFR 1.114

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on July 23, 2007 has been entered.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on July 23, 2007 was filed after the mailing date of the Notice of Allowance on July 2, 2007. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Allowable Subject Matter

- 3. Claims 1-11 and 16-27 are allowed.
- 4. The following is an examiner's statement of reasons for allowance: a search of the prior art failed to disclose or reasonably suggest a semiconductor device, or electronic equipment comprising the semiconductor device, comprising: a first semiconductor element including at least one thin film transistor and a light emitting element electrically connected to the first semiconductor element; a second semiconductor element including at least one thin film transistor and a light receiving element electrically connected to the second semiconductor element;

Application/Control Number: 10/775,328

Art Unit: 2891

wherein the first semiconductor element and the second semiconductor element are stacked and a resin film is formed between the first semiconductor element and the second semiconductor element; wherein the light emitting element comprises a first electrode, a second electrode, and an electro-luminescent layer formed between the first electrode and the second electrode, and wherein the first electrode, the electro-luminescent layer, and the second electrode are overlapped each other, as recited by claims 1-11.

Ovshinsky et al. '471 discloses a semiconductor device (figs. 6A & 6B; col. 18, line 58 – col. 19, line 66) comprising a stacked thin film light emitting element (layer 158 of device 232) and a light receiving element (layer 42 of device 234), with an adhesive film (leveling film, 220) and an insulating film (236) formed between semiconductor circuits, but the reference does not teach the light emitting element and the light receiving element to each be electrically connected to a thin film transistor, nor does the reference teach the stacked elements to have a resin formed between them.

Kohashi '056, Thillays '877, Miyoshi et al. '821, Stein '695, Yasumoto et al. '083, Lebby et al. '245, Vu et al. '953, Hayashi et al. '699, Spaeth et al. '559, and Haas et al. '214 disclose stacked optoelectronic coupling devices, but the references do not teach the light emitting element and the light receiving element to each be electrically connected to a thin film transistor, nor does the reference teach stacked elements having a resin formed between them.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should

Art Unit: 2891

preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven J. Fulk whose telephone number is (571) 272-8323. The examiner can normally be reached on Monday through Friday, 9:30am to 6:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bill Baumeister can be reached on (571) 272-1722. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pairdirect.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

2. WILLIAM BAOD

SOURRVISORY PROPERTY EXAM

TECHNOLOGY CENTE

Steven J. Fulk Patent Examiner Art Unit 2891 August 20, 2007